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DATE MAILED: 10/03/2005

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/616,794	07/10/2003	Robert M. R. Neff	10021118-1	1974
75	10/03/2005		EXAM	INER
AGILENT TECHNOLOGIES, INC.			NGUYEN, PATRICIA T	
Legal Departme	ent, DL429			
Intellectual Property Administration			ART UNIT	PAPER NUMBER
P.O. Box 7599			2817	
Loveland CO	20537 ₋ 0500			

Please find below and/or attached an Office communication concerning this application or proceeding.

			HA
	Application No.	Applicant(s)	
	10/616,794	NEFF, ROBERT M. R.	
Office Action Summary	Examiner	Art Unit	
	Patricia T. Nguyen	2817	
The MAILING DATE of this communication ap Period for Reply	pears on the cover sheet wi	th the correspondence address	
A SHORTENED STATUTORY PERIOD FOR REPL WHICHEVER IS LONGER, FROM THE MAILING ID. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period. - Failure to reply within the set or extended period for reply will, by statuth Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNIC .136(a). In no event, however, may a red d will apply and will expire SIX (6) MON te, cause the application to become AB	CATION. eply be timely filed THS from the mailing date of this communication. ANDONED (35 U.S.C. § 133).	
Status			
1) Responsive to communication(s) filed on	·		
2a)⊠ This action is FINAL . 2b)□ Thi	is action is non-final.		
3) Since this application is in condition for allows closed in accordance with the practice under	•	•	
Disposition of Claims			
 4) Claim(s) 1-25 is/are pending in the application 4a) Of the above claim(s) is/are withdress. 5) Claim(s) 1-24 is/are allowed. 6) Claim(s) 25 is/are rejected. 7) Claim(s) is/are objected to. 			
8) Claim(s) are subject to restriction and/	or election requirement.		
Application Papers			
9) The specification is objected to by the Examin 10) The drawing(s) filed on is/are: a) ac		by the Evaminer	
Applicant may not request that any objection to the			
Replacement drawing sheet(s) including the corre			
11) The oath or declaration is objected to by the E			
Priority under 35 U.S.C. § 119			
12) ☐ Acknowledgment is made of a claim for foreig a) ☐ All b) ☐ Some * c) ☐ None of:	ın priority under 35 U.S.C. §	119(a)-(d) or (f).	
1. Certified copies of the priority documer	nts have been received.		
Certified copies of the priority documer			
3. Copies of the certified copies of the pri		received in this National Stage	
application from the International Bure	•	received	
* See the attached detailed Office action for a lis	s of the certified copies not	ieceiveu.	
Attachment(s)		(575.445)	
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)		Summary (PTO-413) s)/Mail Date	
3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date		nformal Patent Application (PTO-152)	

DETAILED ACTION

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 25 is rejected under 35 U.S.C. 103(a) as being unpatentable over Wojewoda et al., U.S. Patent # 5,731,742.

Fig. 8A of Wojewoda et al. discloses a circuit comprising: transistor 150 that connects to the junction of emitters of the differential transistors 150 can be read as a tail current sink; differential transistors 150 that connect to the drain of tail current sink transistor 150 can be read as a differentially connected pair of FETs wherein the differential transistors 150 can be read as main FET and transistor 32 (auxiliary FET) as well as the switching FET connected to the drain of transistor 32 can be read as a switchable tuning element; transistors 150, 2, 4, 6, 8, 16, 32 and circuit 144 can be read as a composite FET.

Although Wojewoda et al. does not mention that the switch is selected from the group consisting of mechanical switches, bridgeable links, and fusible links; it would have been obvious at the time the invention was made to a person having ordinary skill in the art to substitute one type of switch for the other since such substitution is a well known practice in the art and this is a matter of design choice to have an optimum working condition for the circuit.

Claim 25 is rejected under 35 U.S.C. 102(e) as being anticipated by Beatson, U.S. Patent # 6,531,916 B2.

Figs. 2, 3, and 4 discloses a circuit comprising: in Fig. 2, tail current sink 224 can be read as a tail current sink; in Fig. 3 differential transistors 328, 330 can be read as a differentially connected pair of FETs wherein the differential transistor 328 can be read as main FET and transistor 326 (auxiliary FET) as well as the switching FET 346 connected to the drain of transistor 326 can be read as a switchable tuning element; transistors 320-328 and 340-346 can be read as a composite FET.

Although Beatson does not mention that the switch is selected from the group consisting of mechanical switches, bridgeable links, and fusible links; it would have been obvious at the time the invention was made to a person having ordinary skill in the art to substitute one type of switch for the other since such substitution is a well known practice in the art and this is a matter of design choice to have an optimum working condition for the circuit.

Allowable Subject Matter

Claims 1-24 are allowed.

Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

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extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Patricia T. Nguyen whose telephone number is (703) 308-1927. The examiner can normally be reached on 6:30 AM - 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on 703-309-4940. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PTN

September 29, 2005

PATRICIA NGUYEN
PRIMARY EXAMINER

Patricia Ngrugen